IN THE SPECIFICATION

Please amend the specification as follows.

Please amend the title as follows:

METHOD AND APPARATUS FOR IDENTIFYING PROCESSING UNITS TO A NETWORK USING NETWORK ADDRESSES OBTAINED FROM REMOVABLE DATA CARRIERS

Please replace the three paragraphs starting on page 16, line 32 and ending on page 17, line 12 with the following three amended paragraphs:

Figure [[16]] 11 is a schematic block diagrammatic representation of the system architecture for the processing unit of Figure [[15]] 10.

In this particular example, the CPU 416 of Figure [[16]] 11 is an UltraSparc processor 452 available from Sun Microsystems, Inc. In other embodiments other processors could, of course, be used. A configurable clock generator 454 is provided to supply various system clocks. A vectored interrupt Controller (I-Chip2) 456 is provided for handling interrupts. Also provided is a configurable core Voltage Regulator Module (VRM) 458.

Four sockets 425 are provided for commodity DIMMs 460. Connections are provided for a 72 bit data path with Error Correction Codes (ECC). A Personal Computer Interconnect (PCI) bus architecture is provided that includes an Advance PCI Bridge (APB) 462. This PCI Bridge 462 concentrates two secondary PCI busses (PCI Bus A and PCI Bus B) onto a primary PCI bus (PCI Bus) as represented in Figure [[16]] 11.